

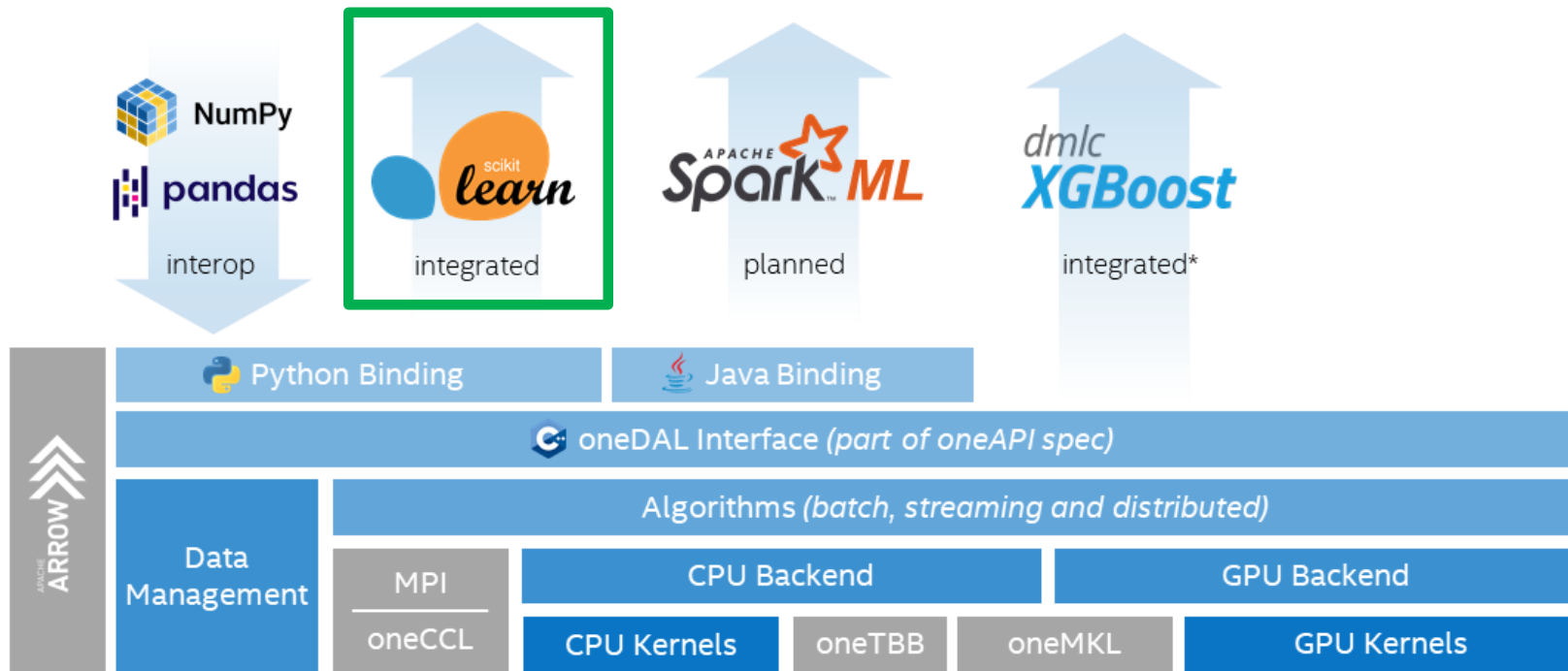


SPEEDUP CLASSIC MACHINE LEARNING AND DATA ANALYTICS WITH INTEL



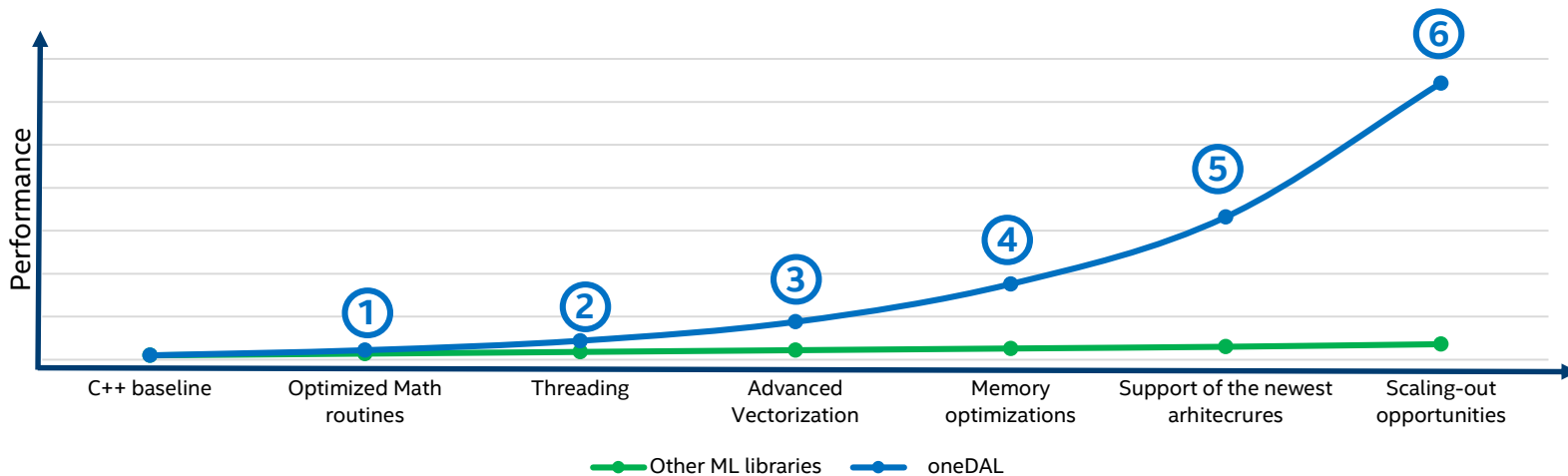
oneAPI Data Analytics Library (oneDAL)

Optimized building blocks for all stages of data analytics on Intel Architecture



GitHub: <https://github.com/oneapi-src/oneDAL>

What makes oneDAL faster?



1 The best performance on Intel Architectures with oneMKL (Intel® MKL) vs. less performance OS BLAS/LAPACK libs

2 oneDAL targets to many-core systems to achieve the best scalability on Intel® Xeon, other libs mostly target to client versions with small amount of cores

3 oneDAL uses the latest available vector-instructions on each architecture, enables them by compiler options, intrinsics. Usually other ML libs build application without vector-instructions support or sse4.2 only.

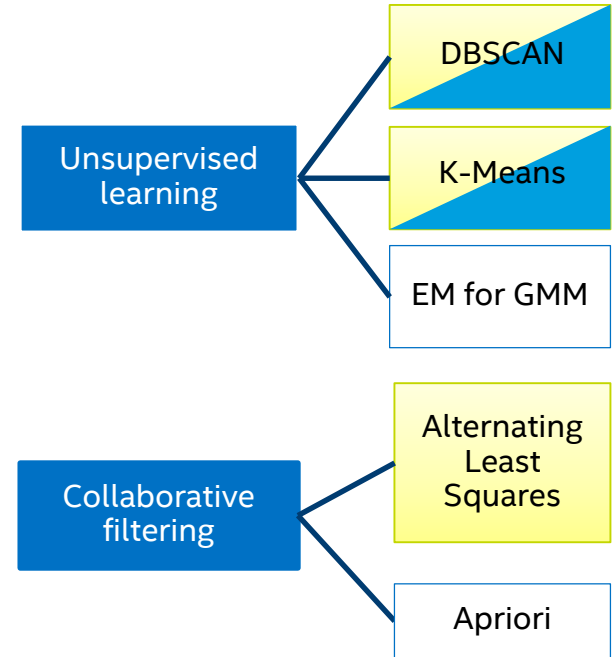
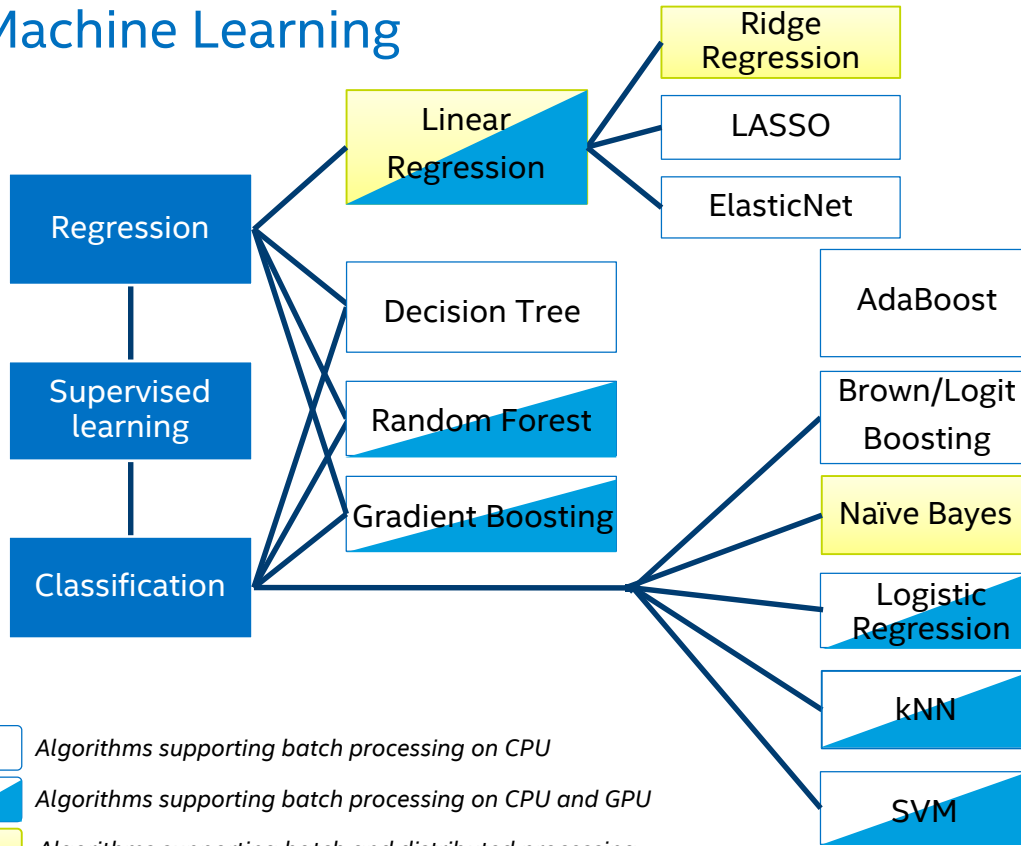
4 oneDAL uses the most efficient memory optimization practices: minimally access memory, cache access optimizations, SW memory prefetching. Usually Other ML libs don't make low-level optimizations.




5 oneDAL enables new instruction sets and other HW features even before official HW launch. Usually other ML libs do this with long delay.

6 oneDAL provides distributed algorithms which scale on many nodes

oneDAL Algorithms

Machine Learning



-  Algorithms supporting batch processing on CPU
-  Algorithms supporting batch processing on CPU and GPU
-  Algorithms supporting batch and distributed processing

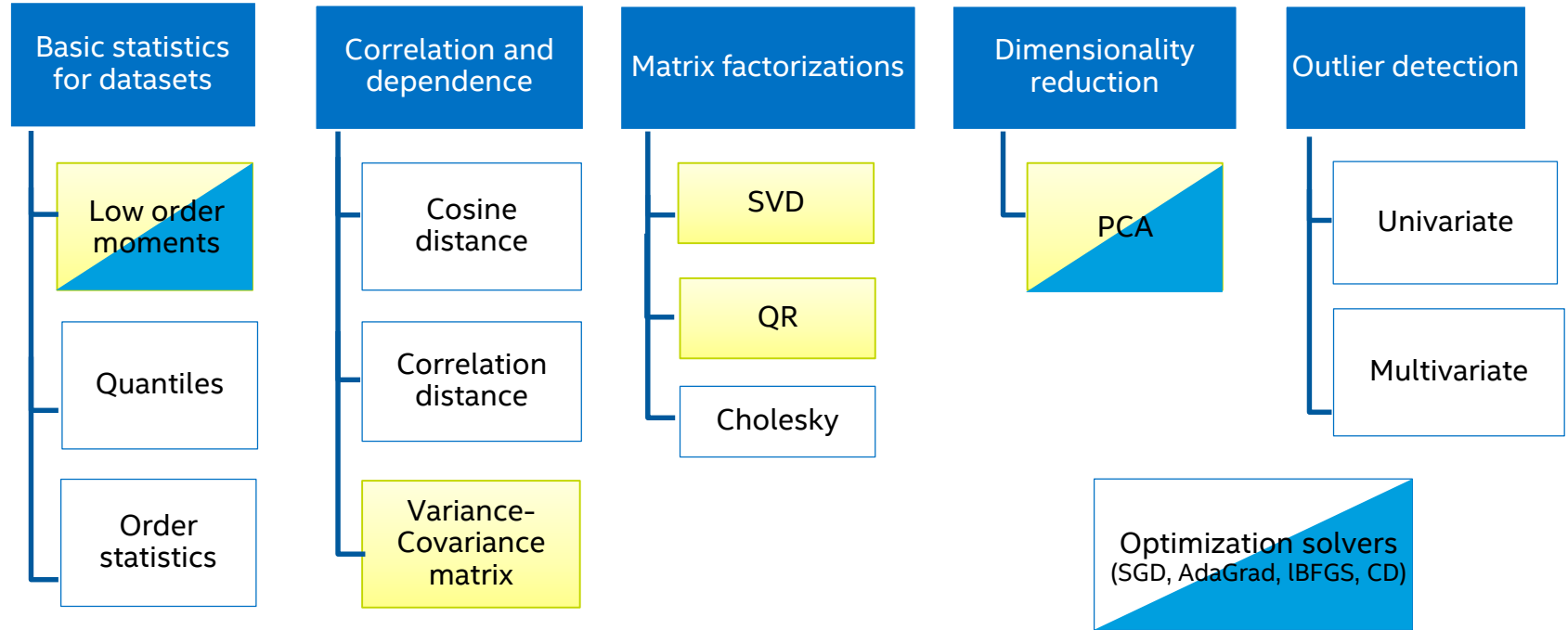
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
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oneDAL Algorithms

Data Transformation and Analysis



-  Algorithms supporting batch processing on CPU
-  Algorithms supporting batch processing on CPU and GPU
-  Algorithms supporting batch and distributed processing

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SCIKIT-LEARN ACCELERATION

Intel Distribution for Python (IDP) Scikit-learn

Common Scikit-learn

```
from sklearn.svm import SVC

X, Y = get_dataset()

clf = SVC().fit(X, y)
res = clf.predict(X)
```

Scikit-learn mainline

Scikit-learn with Intel CPU opts

```
import daal4py as d4p
d4p.patch_sklearn()

from sklearn.svm import SVC

X, Y = get_dataset()

clf = SVC().fit(X, y)
res = clf.predict(X)
```

Available through Intel conda
(conda install daal4py -c intel)

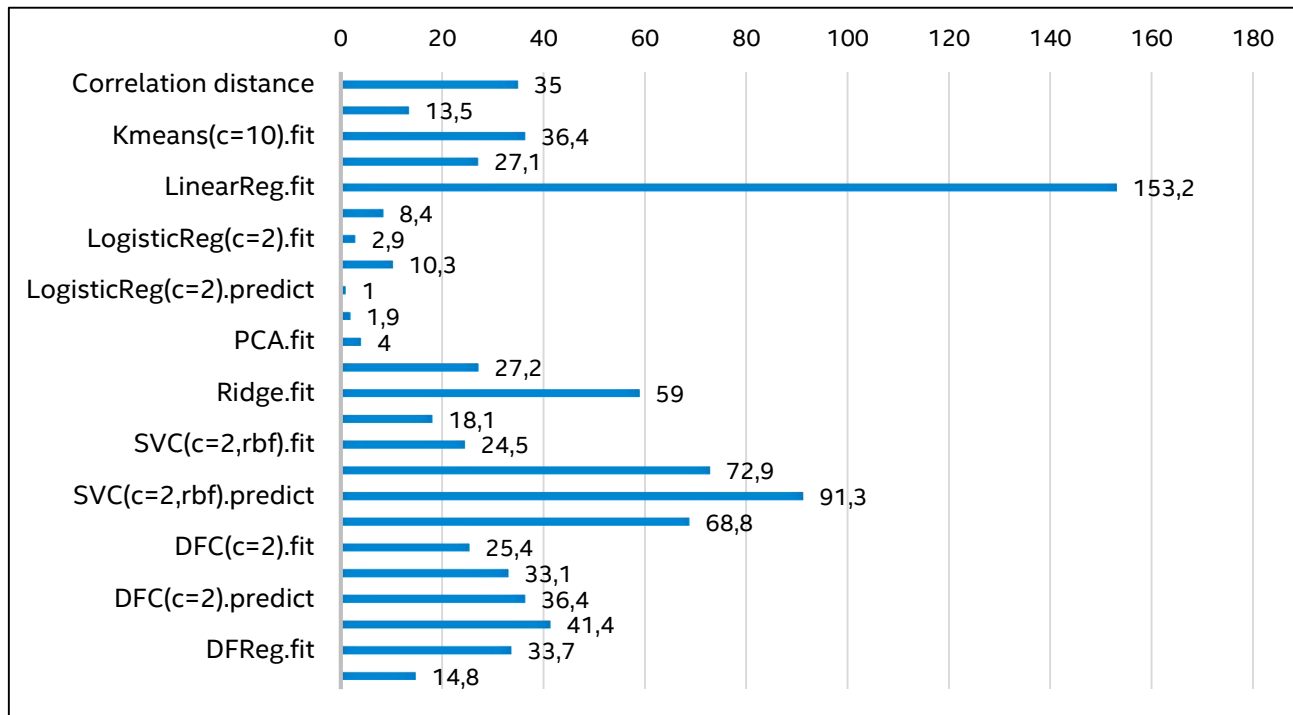
Same Code,
Same Behavior

 PASSED

- Scikit-learn, not scikit-learn-like
- Scikit-learn conformance (mathematical equivalence) defined by Scikit-learn Consortium, continuously vetted by public CI

Intel optimized Scikit-Learn

Intel® Distribution for Python* Scikit-learn* acceleration



Same Code,
Same Behavior

 PASSED

- Scikit-learn, not scikit-learn-like
- Scikit-learn conformance (mathematical equivalence) defined by Scikit-learn Consortium, continuously vetted by public CI

HW: Intel Xeon Platinum 8276L CPU @ 2.20GHz, 2 sockets, 28 cores per socket;

Details: <https://medium.com/intel-analytics-software/accelerate-your-scikit-learn-applications-a06cacf44912>

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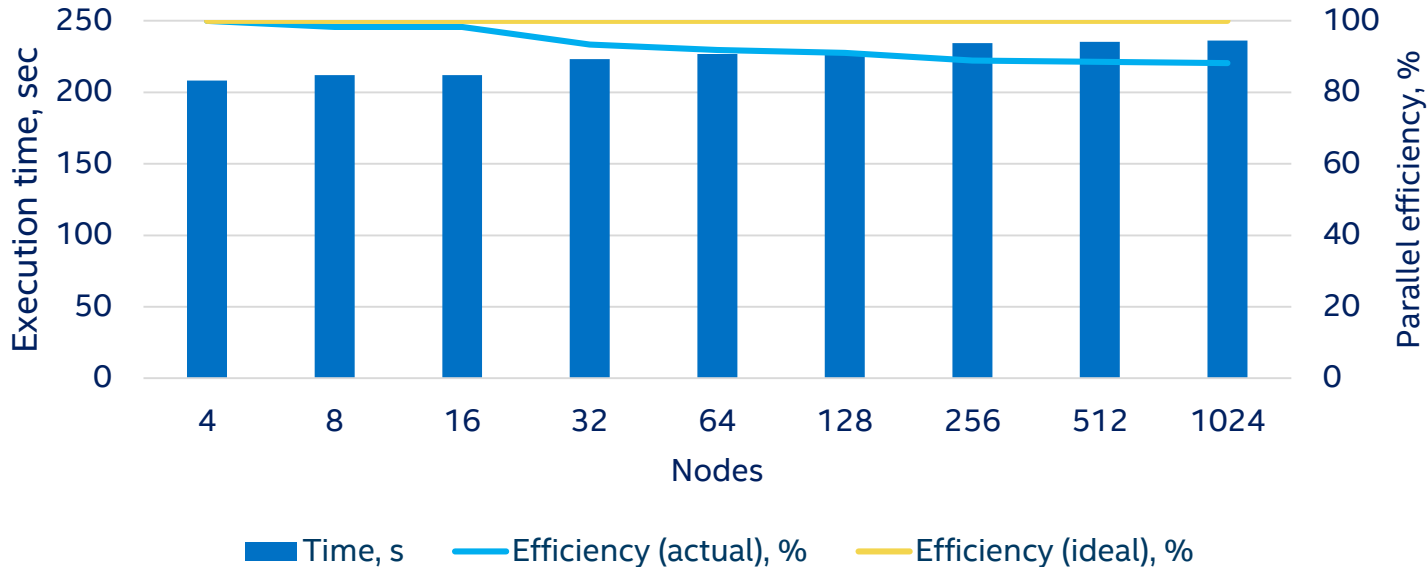
Available algorithms

Accelerated IDP Scikit-learn algorithms:

- Linear/Ridge Regression
- Logistic Regression
- ElasticNet/LASSO
- PCA
- K-means
- DBSCAN
- SVC
- `train_test_split()`, `assume_all_finite()`
- Random Forest Regression/Classification - DAAL 2020.3
- kNN (kd-tree and brute force) - DAAL 2020.3

SCALE OUT ACCELERATION

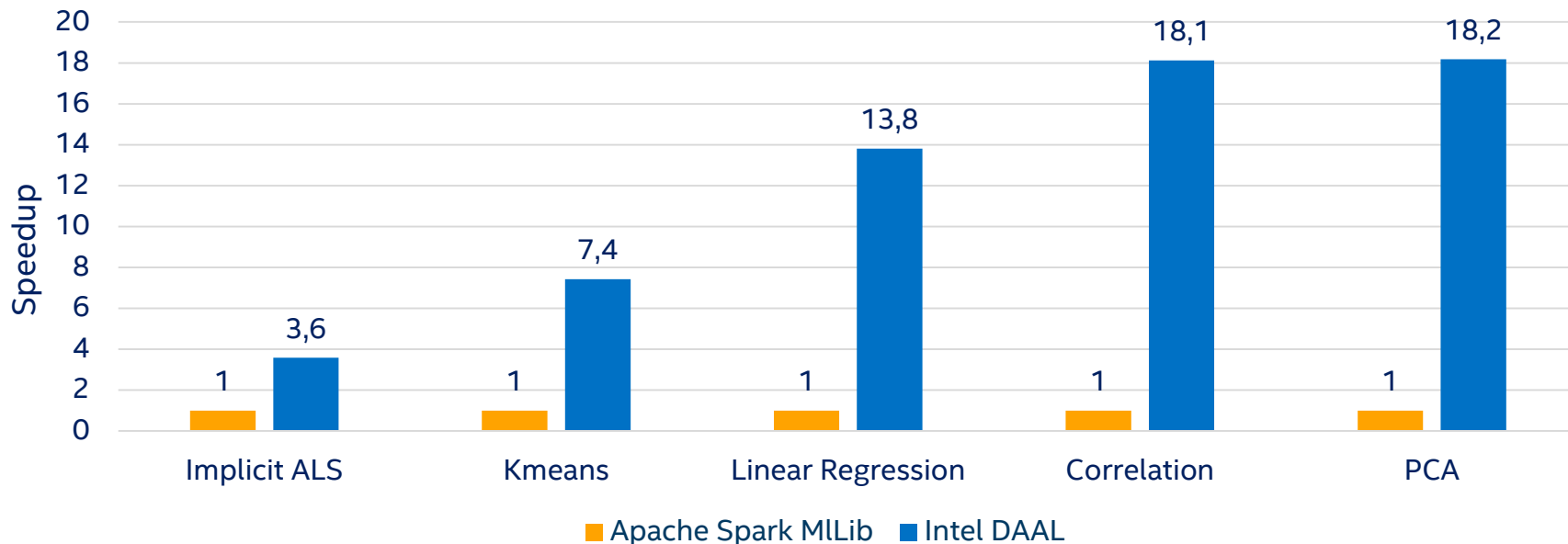
Intel® DAAL K-means fit, week scaling results (87.44GB/node, 84 features, 8 clusters, 100 iterations, float32)



Source: <https://arxiv.org/abs/1909.11822>

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Configuration: Testing by Intel as of 09/25/2019. 7 x m5.2xlarge AWS instances, Intel® Data Analytics Acceleration Library 2019.3 (Intel® DAAL); Intel Xeon Processor E5-2698 v3 @ 2.3GHz, 2 sockets, 16 cores per socket, MPI4Py (3.0.0), Intel® Distribution Of Python (IDP) 3.6.8, float, Source: <https://arxiv.org/abs/1909.11822>
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Intel® DAAL 2020 vs Apache Spark* MLlib performance (Higher is better)



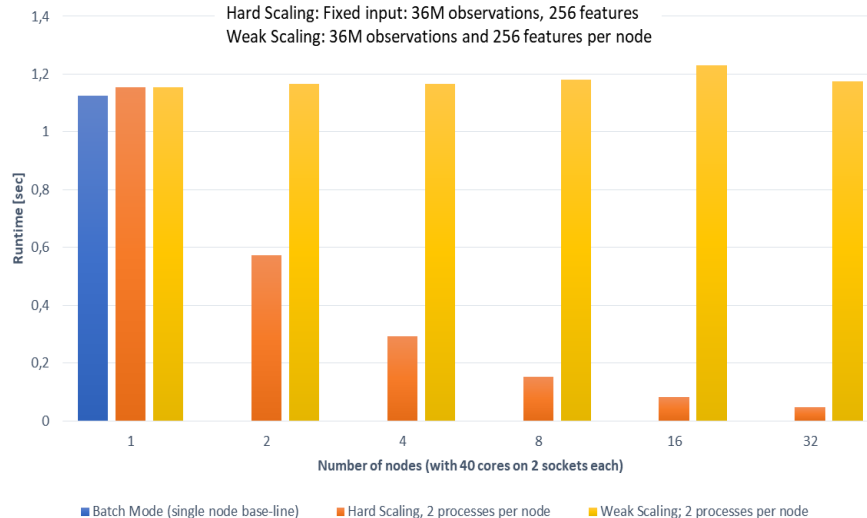
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Configuration: Testing by Intel as of **11/11/2019**. 7 x m5.2xlarge AWS instances, Intel® Data Analytics Acceleration Library 2020 (Intel® DAAL); Correlation (# samples = 10M, # features = 1000, (Intel® DAAL=35.2s, MLlib=638.2s)), PCA (# samples = 10M, # features = 1000 (Intel® DAAL=35.2s, MLlib=639.8s)), implicit ALS (# users = 1M, # items = 1M, # factors = 100, # iterations = 1 (Intel® DAAL=37.6s, MLlib=134.9s)), Linear Regression (# samples = 100M, # features = 50 (Intel® DAAL=16.3s, MLlib=224.5s)), k-means (# samples = 100M, # features = 50, # clusters = 10, # iterations = 100 (Intel® DAAL=211s, MLlib=1567.3s))
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Working in distributed environment

Hardware	Intel(R) Xeon(R) Gold 6148 CPU @ 2.40GHz, EIST/Turbo on
Hardware	2 sockets, 20 Cores per socket
Hardware	192 GB RAM
Hardware	16 nodes connected with Infiniband
Operating System	Oracle Linux Server release 7.4
Data Type	double

daal4py Linear Regression Distributed Scalability

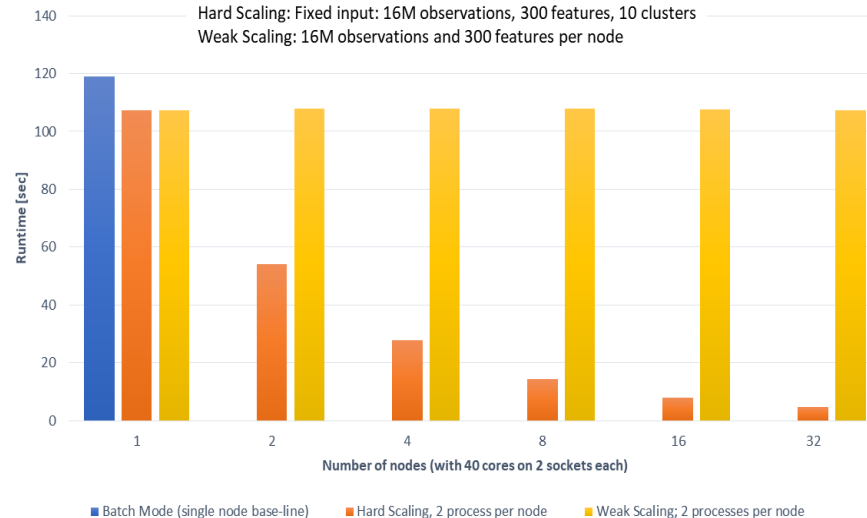
Hard Scaling: Fixed input: 36M observations, 256 features
Weak Scaling: 36M observations and 256 features per node



On a 32-node cluster (1280 cores) daal4py computed linear regression of 2.15 TB of data in 1.18 seconds and 68.66 GB of data in less than 48 milliseconds.

daal4py K-Means Distributed Scalability

Hard Scaling: Fixed input: 16M observations, 300 features, 10 clusters
Weak Scaling: 16M observations and 300 features per node



On a 32-node cluster (1280 cores) daal4py computed K-Means (10 clusters) of 1.12 TB of data in 107.4 seconds and 35.76 GB of data in 4.8 seconds.

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INTEL DAAL USAGE EXAMPLES

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